



TS99-132CC

Application no 10/623,910

To: Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Subject: Serial No.: 10/623,910
Filing Date: 07/18/2003
Inventor: Shue et al.
Examiner: Ho, Tu-Tu
Art Unit: 2818

Title: Method for Integrating Low-K Materials in Semiconductor
Fabrication

RESPONSE TO OFFICE ACTION

Dear Sirs:

This is in response to the office action dated December 22, 2003. Please
amend the above identified application for patent as follows:

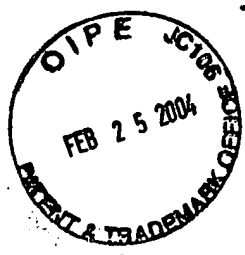
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United
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P.O. Box 1450, Alexandria, VA 22313-1450, on February 23, 2004

Signature/Date Stephen B. Ackerman February 23, 2004

Stephen B. Ackerman, Reg #37,761



TS99-132CC

Application no 10/623,910

February 12, 2004

Subject:	Application No.	:	10/623,910
	Applicant	:	Shue et al.
	Filing Date	:	07/18/2003
	TC/A.U.	:	2818
	Examiner	:	Ho, Tu-Tu
	Docket No.	:	TS99-132CC
	Customer No.	:	

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

This paper is a repeat of correspondence January 19, 2004 and corrects the omission of claims status, namely herein shown as (ORIGINAL).

In response to the Office Action of 12-22-2003, please amend the above-identified application as follows:

Amendments to the Specification are found on page 3 of the paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 4 of the paper.

Remarks/Arguments begin on page 10 of the paper.